

Model-Checking Timed Temporal Logics

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Oxford University Computing Laboratory – UK

Based on joint works with Fabrice Chevalier, Nicolas Markey,
Joël Ouaknine and James Worrell

Outline

1. Introduction
2. Definition of the logics
3. The timed automaton model
4. The model-checking problem
5. Some interesting fragments
6. Conclusion

Model-checking

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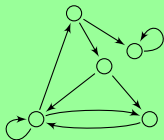


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Model-checking

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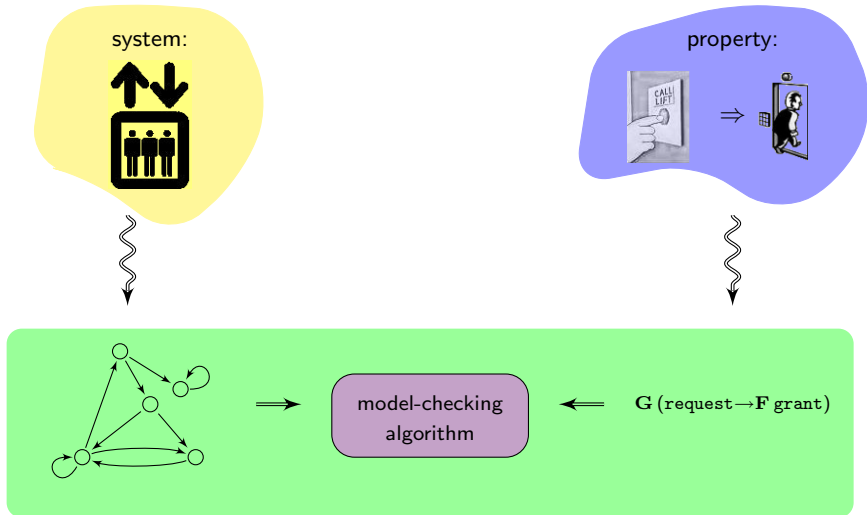


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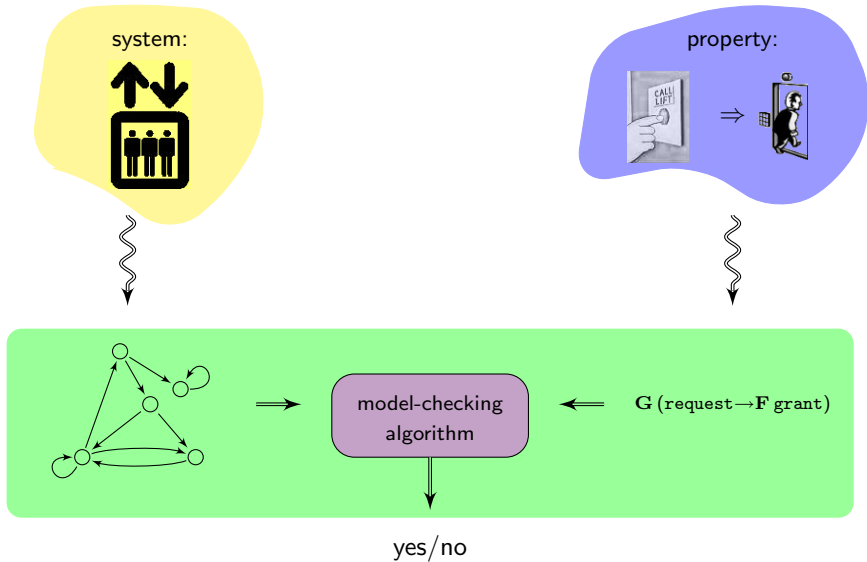


$G (\text{request} \rightarrow F \text{ grant})$

Model-checking



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The untimed (linear-time) framework

Linear-time temporal logic [Pnu77]

$\text{LTL} \ni \varphi ::= p \mid \varphi \wedge \varphi \mid \varphi \vee \varphi \mid \neg \varphi \mid \mathbf{X} \varphi \mid \varphi \mathbf{U} \varphi$

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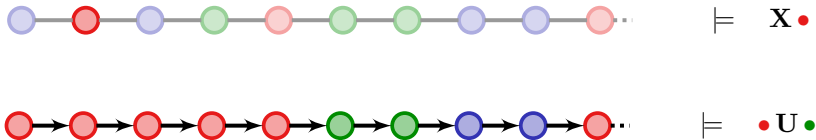
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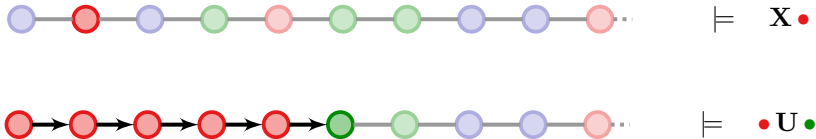
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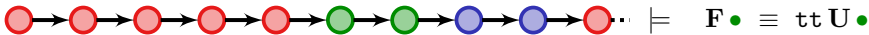
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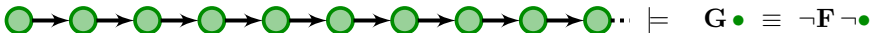
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- ▶ a more complex property:

$$(\bullet \wedge (\mathbf{F}\bullet \vee \mathbf{G}\bullet)) \mathbf{U}\bullet$$

Adding timing requirements

- ▶ Need for **timed models**
 - ▶ the behaviour of most systems depends on time;
 - ▶ faithful modelling has to take time into account.
- ☞ **timed automata, time(d) Petri nets, timed process algebras...**

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- ▶ Need for **timed specification languages**
 - ▶ the behaviour of most systems depends on time;
 - ▶ untimed specifications are not sufficient
(for instance, bounded response timed, etc...)

☞ **TCTL, MTL, TPTL, timed μ -calculus...**

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[Koy90]

$$\text{MTL} \ni \varphi ::= a \mid \neg\varphi \mid \varphi \vee \varphi \mid \varphi \wedge \varphi \mid \varphi \mathbf{U}_I \varphi$$

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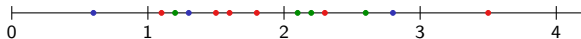
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- ▶ Can be interpreted over **finite** or **infinite** behaviours
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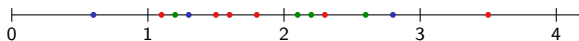
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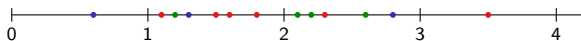


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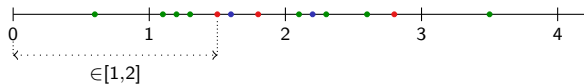
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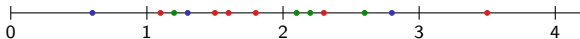
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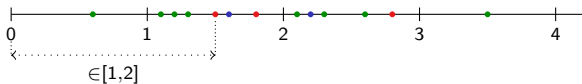
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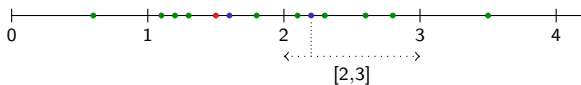


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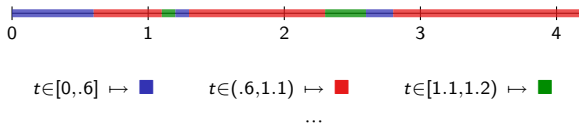
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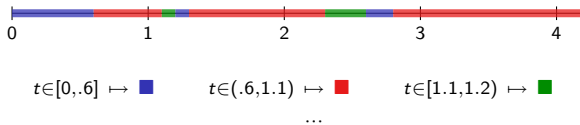
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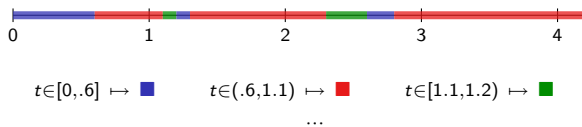
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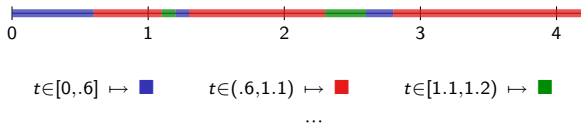


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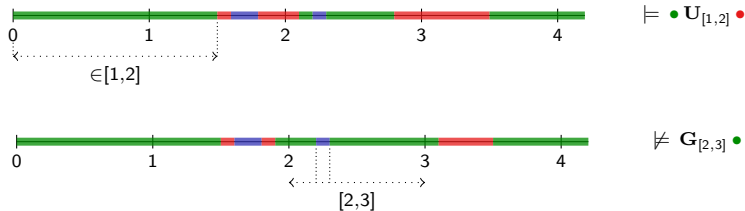


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Some examples

- ▶ “Every problem is followed within 56 time units by an alarm”

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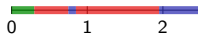
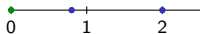
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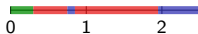
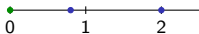
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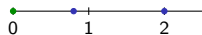
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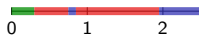
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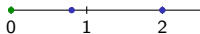
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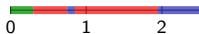
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- ▶ in the pointwise semantics, $F_{=2} \bullet \not\equiv F_{=1} F_{=1} \bullet$
- ▶ in the continuous semantics, $F_{=2} \bullet \equiv F_{=1} F_{=1} \bullet$

Some further extensions

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LTL+Past is as expressive as LTL [Kam68,GPSS80].

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- ▶ This is wrong in the **continuous** semantics!

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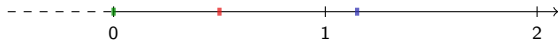


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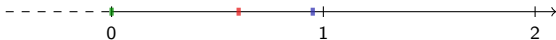


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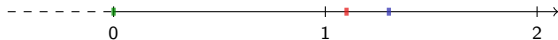


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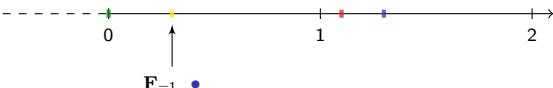


$$\mathbf{G} \bullet \rightarrow \left\{ \begin{array}{l} \mathbf{F}_{\leq 1} \bullet \wedge \mathbf{F}_{[1,2]} \bullet \\ \vee \\ \mathbf{F}_{\leq 1} (\bullet \wedge \mathbf{F}_{\leq 1} \bullet) \\ \vee \end{array} \right.$$

The TPTL formula

$$\mathbf{G}(\bullet \rightarrow x.\mathbf{F}(\bullet \wedge \mathbf{F}(\bullet \wedge x \leq 2)))$$

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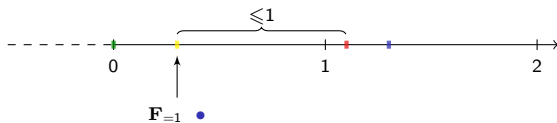
$\mathbf{F}_{=1} \bullet$

$$\mathbf{G} \bullet \rightarrow \left\{ \begin{array}{l} \mathbf{F}_{\leq 1} \bullet \wedge \mathbf{F}_{[1,2]} \bullet \\ \vee \\ \mathbf{F}_{\leq 1} (\bullet \wedge \mathbf{F}_{\leq 1} \bullet) \end{array} \right.$$

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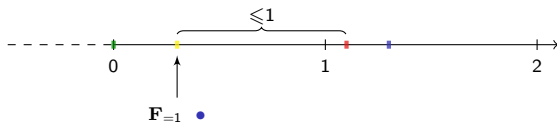


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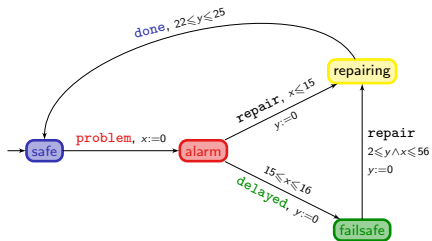


$$\mathbf{G} \bullet \rightarrow \left\{ \begin{array}{l} \vee \\ \mathbf{F}_{\leq 1} \bullet \wedge \mathbf{F}_{[1,2]} \bullet \\ \vee \\ \mathbf{F}_{\leq 1} (\bullet \wedge \mathbf{F}_{\leq 1} \bullet) \\ \vee \\ \mathbf{F}_{\leq 1} (\mathbf{F}_{\leq 1} \bullet \wedge \mathbf{F}_{=1} \bullet) \end{array} \right.$$

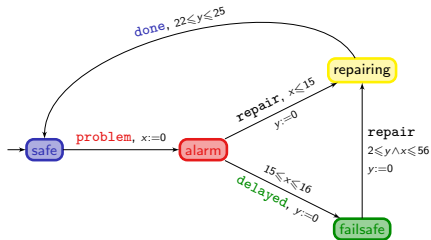
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Timed automata

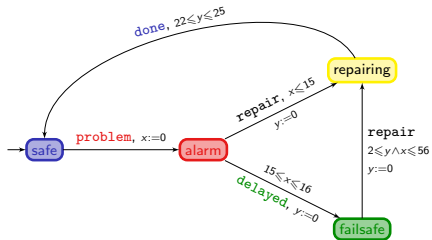


Timed automata



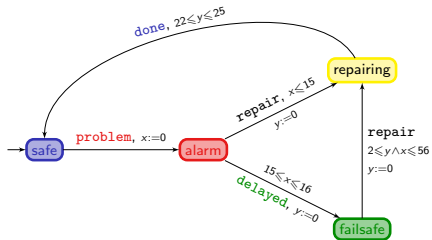
safe
 x 0
 y 0

Timed automata



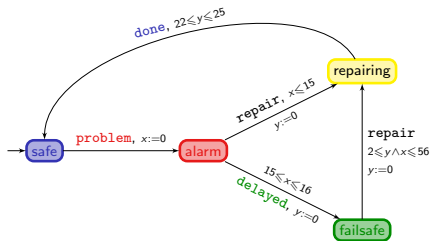
	safe	$\xrightarrow{23}$	safe
x	0		23
y	0		23

Timed automata



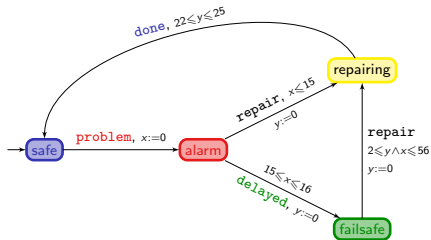
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm
x	0		23		0
y	0		23		23

Timed automata



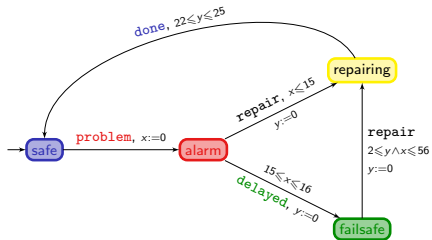
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm
x	0		23		0		15.6
y	0		23		23		38.6

Timed automata



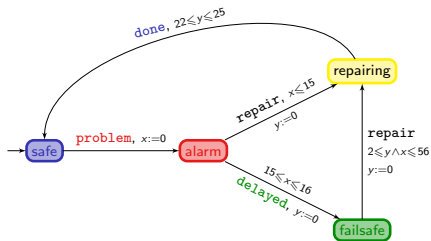
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe
x	0		23		0		15.6		15.6
y	0		23		23		38.6		0

Timed automata



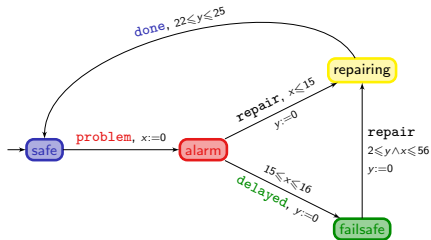
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	$\xrightarrow{2.3}$	failsafe
x	0		23		0		15.6		15.6		17.9
y	0		23		23		38.6		0		2.3

Timed automata



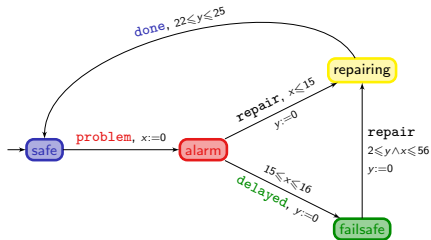
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	$\xrightarrow{2.3}$	failsafe	$\xrightarrow{\text{repair}}$	reparation
x	0		23		0		15.6		15.6		17.9		17.9
y	0		23		23		38.6		0		2.3		0

Timed automata



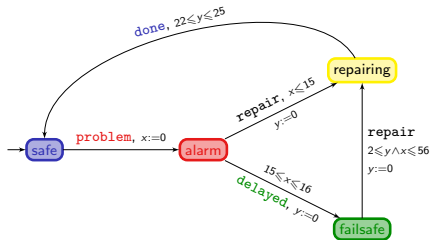
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	$\xrightarrow{2.3}$	failsafe	$\xrightarrow{\text{repair}}$	reparation	$\xrightarrow{22.1}$	reparation
x	0		23		0		15.6		15.6		17.9		17.9		40
y	0		23		23		38.6		0		2.3		0		22.1

Timed automata



	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	$\xrightarrow{2.3}$	failsafe	$\xrightarrow{\text{repair}}$	reparation	$\xrightarrow{22.1}$	reparation	$\xrightarrow{\text{done}}$	safe
x	0		23		0		15.6		15.6		17.9		17.9		40		40
y	0		23		23		38.6		0		2.3		0		22.1		22.1

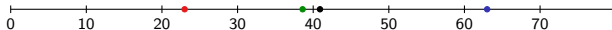
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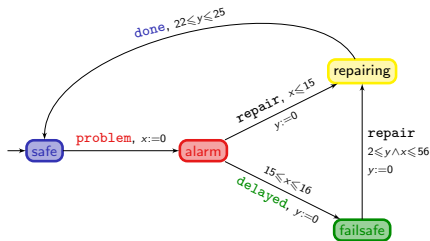
	safe	$\xrightarrow{23}$	safe	$\xrightarrow{\text{problem}}$	alarm	$\xrightarrow{15.6}$	alarm	$\xrightarrow{\text{delayed}}$	failsafe	$\xrightarrow{2.3}$	failsafe	$\xrightarrow{\text{repair}}$	repairing	$\xrightarrow{22.1}$	repairing	$\xrightarrow{\text{done}}$	safe
x	0		23		0		15.6		15.6		17.9		17.9		40		40
y	0		23		23		38.6		0		2.3		0		22.1		22.1

Can be viewed:

- ▶ as the timed word $(\text{problem}, 23)(\text{delayed}, 38.6)(\text{repair}, 40.9)(\text{done}, 63)$



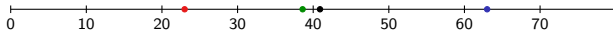
Timed automata



	safe	23	safe	problem	alarm	15.6	alarm	delayed	failsafe	2.3	failsafe	repair	reparation	22.1	reparation	done	safe
x	0	23	23	0	15.6	15.6	15.6	17.9	17.9	17.9	17.9	0	40	22.1	40	40	40
y	0	23	23	23	38.6	0	2.3	0	22.1	22.1	0	0	0	0	0	0	22.1

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- ▶ as the signal



Basic result on timed automata

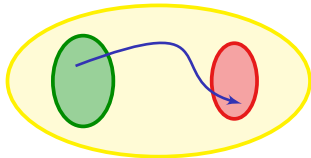
Theorem

The reachability problem is decidable (and **PSPACE**-complete) for timed automata [AD94].

Basic result on timed automata

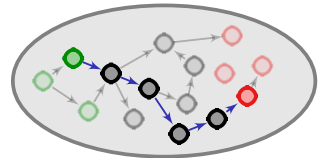
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timed automaton

finite bisimulation

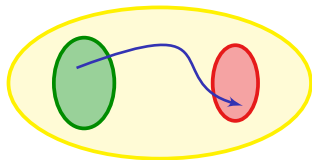
large (but finite) automaton
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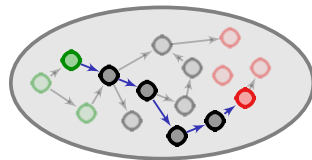
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☞ It can be extended to model-check TCTL [ACD93].

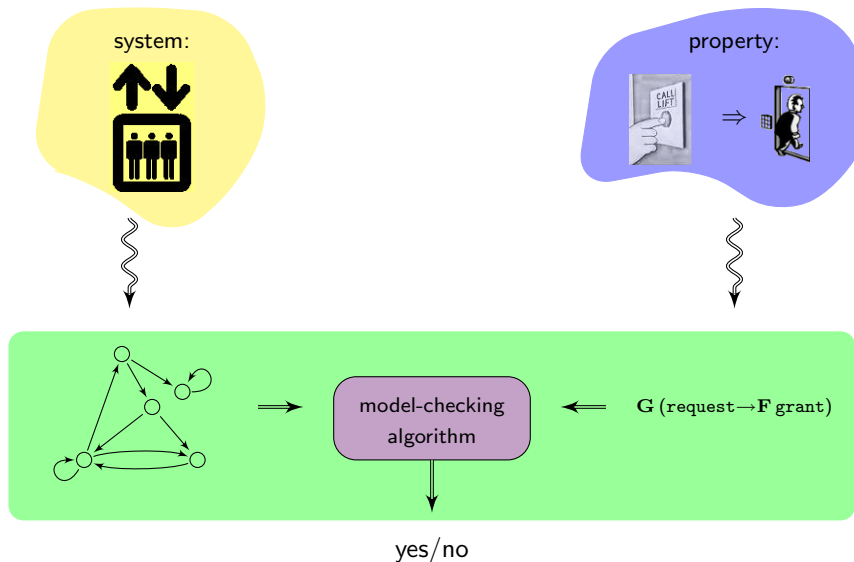
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[ACD93] Alur, Courcoubetis, Dill. Model-checking in dense real-time (I&C, 1993).

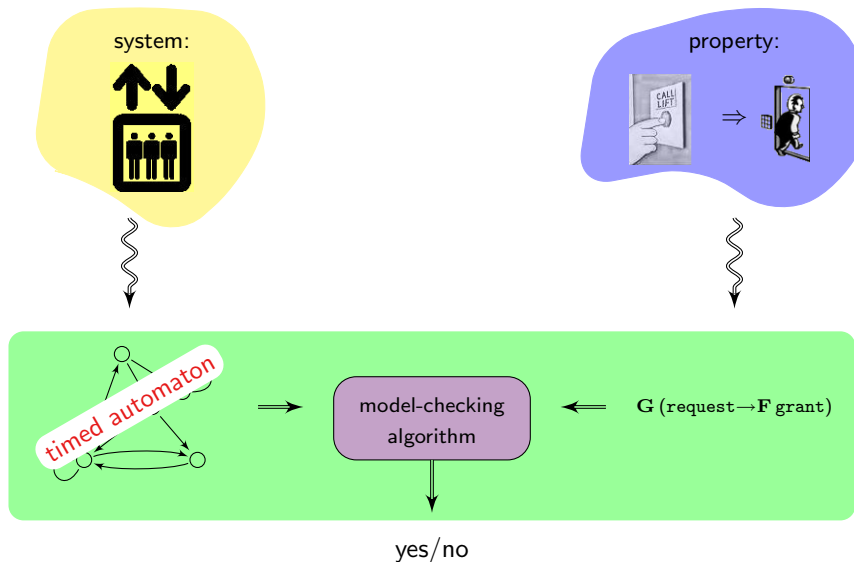
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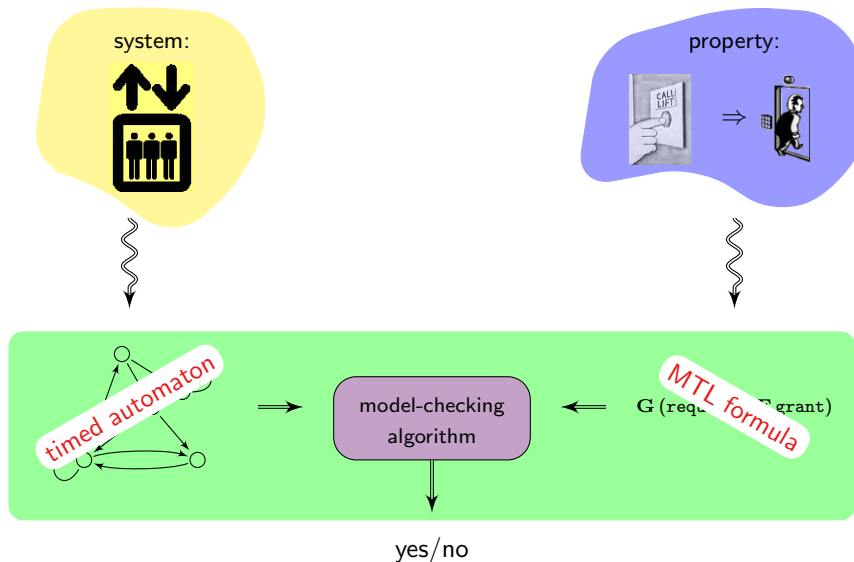
Back to the model-checking problem



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Back to the model-checking problem



Results

Theorem

Over finite runs, the model-checking problem is:

	pointwise sem.	continuous sem.
MTL	decidable, NPR [OW05]	undecidable [AFH96]
MTL+Past	undecidable	undecidable
TPTL	undecidable [AH94]	undecidable [AH94]

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☞ we will explain this high complexity, following [Che07]

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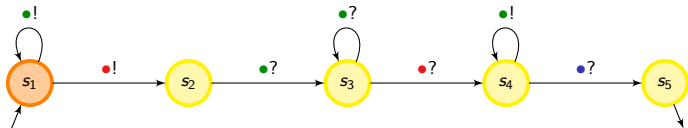
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[Che07] Chevalier. Logiques pour les systèmes temporisés : contrôle et expressivité (PhD Thesis ENS Cachan, June 2007).

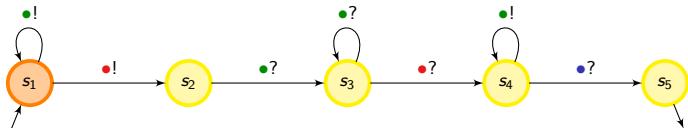
A short visit to channel machines (1)

A channel machine = a finite automaton + a FIFO channel



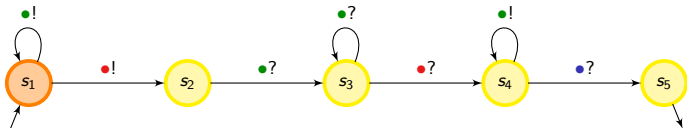
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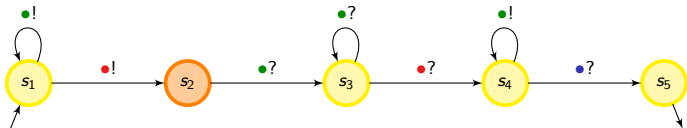
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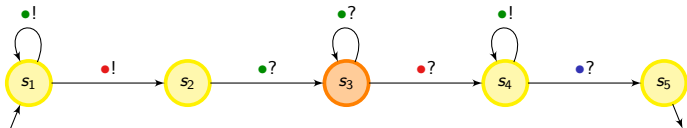
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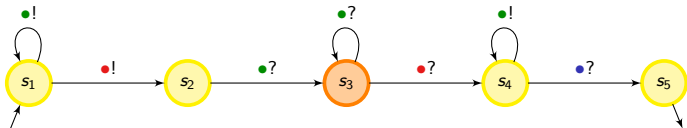
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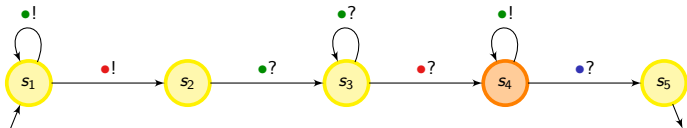
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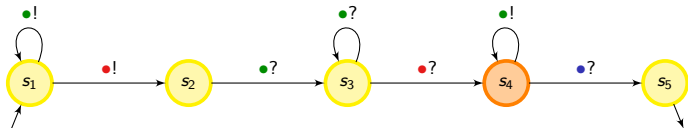
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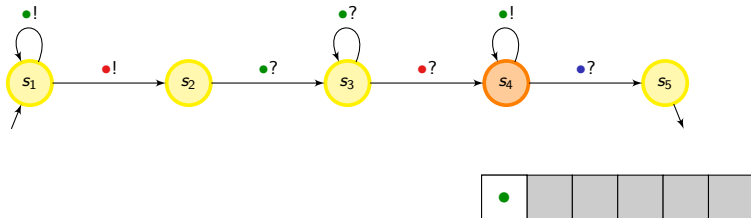
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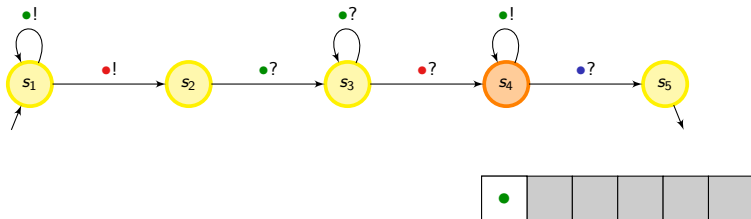
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s_5 is not reachable

A short visit to channel machines (1)

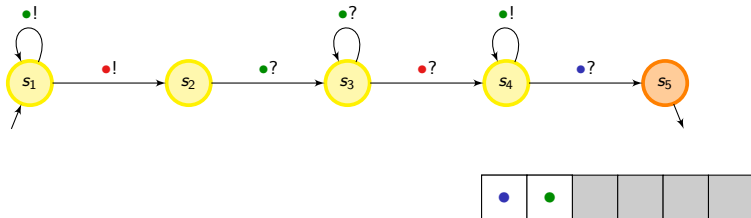
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- ▶ insertion errors: any letter can appear on the channel at any time

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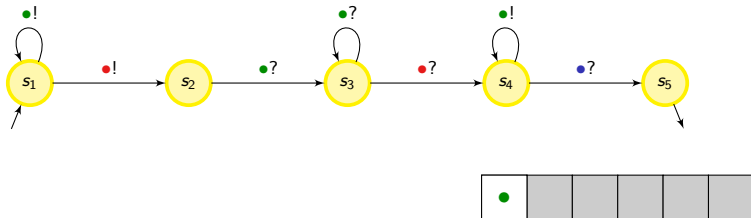
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A short visit to channel machines (2)

Halting problem: is there an execution ending in a halting state?

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Proposition

- ▶ The halting problem is undecidable for channel machines [BZ83].
- ▶ The halting problem is NPR for channel machines with insertion errors [Sch02].

[BZ83] Brand, Zafiropulo. On communicating finite-state machines (Journal of the ACM, 1983).

[Sch02] Schnoebelen. Verifying lossy channel systems has non-primitive recursive complexity (IPL, 2002).

Channel machines and timed words

We encode an execution of a channel machine as a timed word:

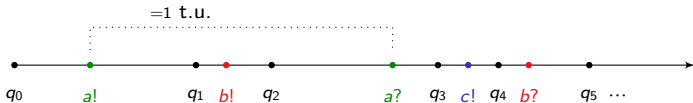
$$(q_0, \varepsilon) \xrightarrow{a!} (q_1, a) \xrightarrow{b!} (q_2, ab) \xrightarrow{a?} (q_3, b) \xrightarrow{c!} (q_4, bc) \xrightarrow{b?} (q_5, c) \dots$$



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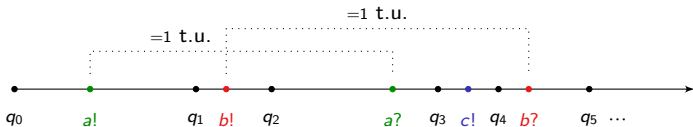
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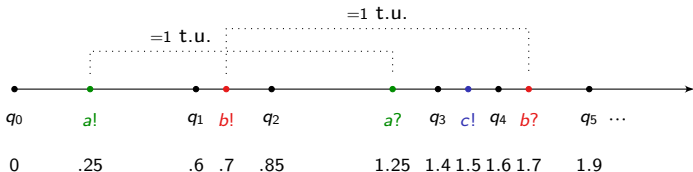
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We will give a formula φ such that

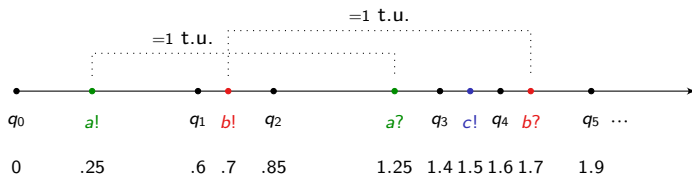
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Constraints satisfied by the timed word

- ▶ states and actions alternate, and the sequence satisfies the rules of the channel machine: **LTL formula**

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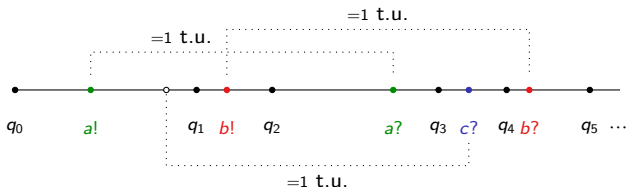
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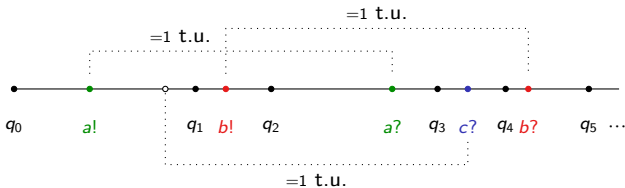
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☞ only encodes a channel machine *with* insertion errors!

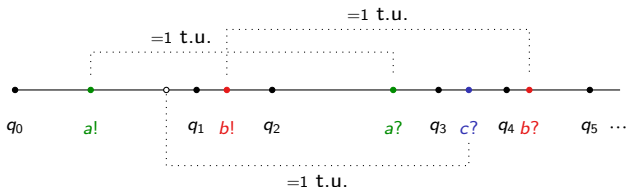
Constraints satisfied by the timed word

- ▶ states and actions alternate, and the sequence satisfies the rules of the channel machine: **LTL formula**
- ▶ the channel is FIFO: for every letter a ,

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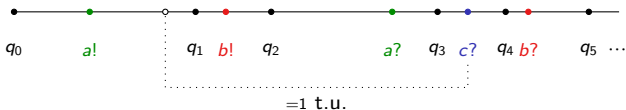
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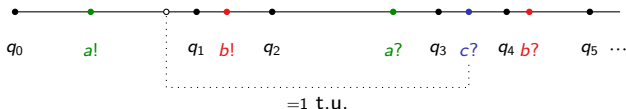
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What we have proved so far

Theorem

Over finite runs, the model-checking problem is:

	pointwise sem.	continuous sem.
MTL	NPR [OW07]	undecidable [AFH96]
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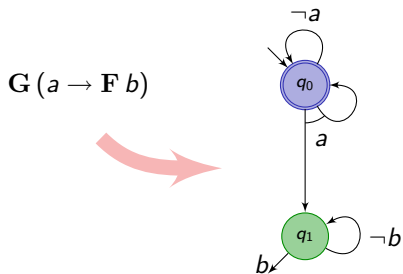
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LTL formulas can be turned into linear alternating (Büchi) automata

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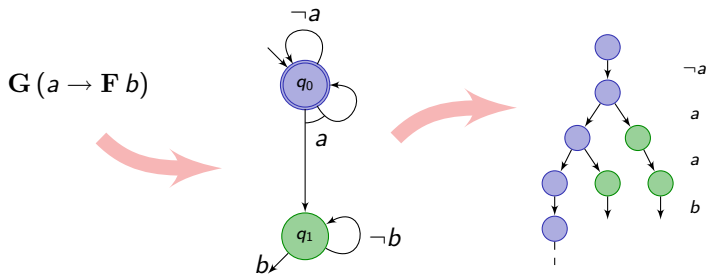
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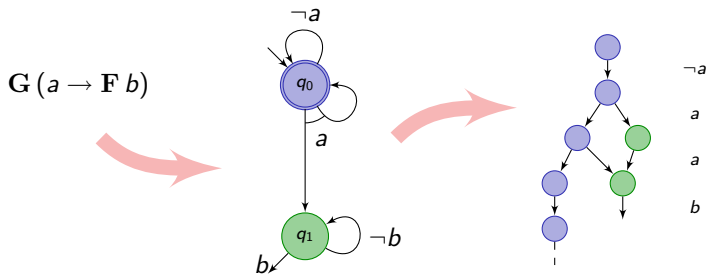
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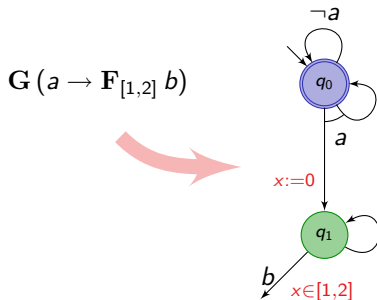
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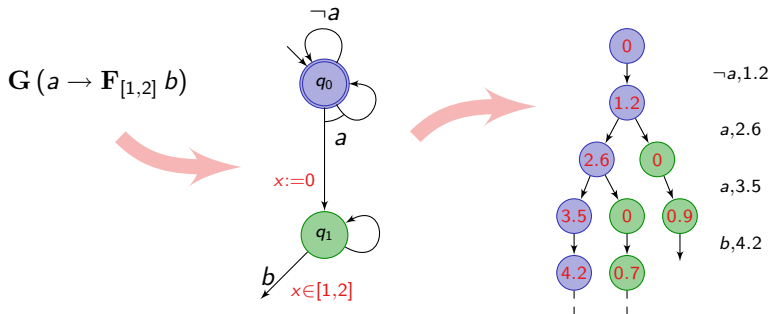
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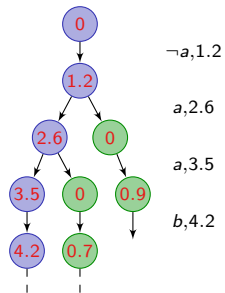


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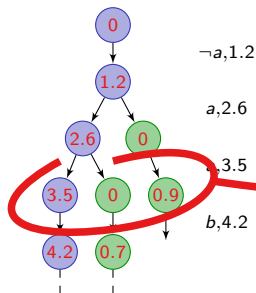
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An abstract transition system

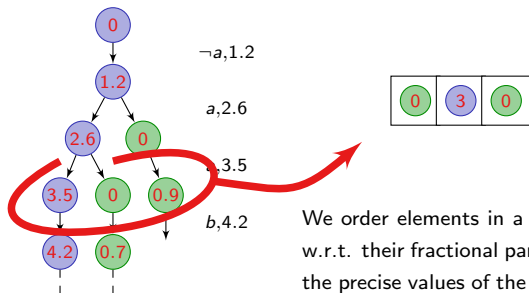


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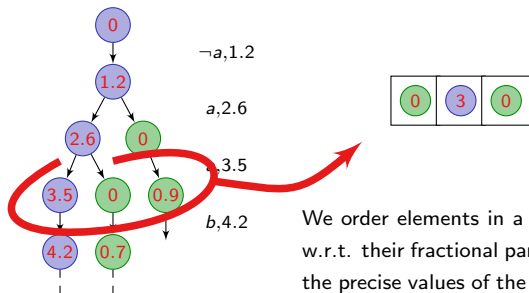
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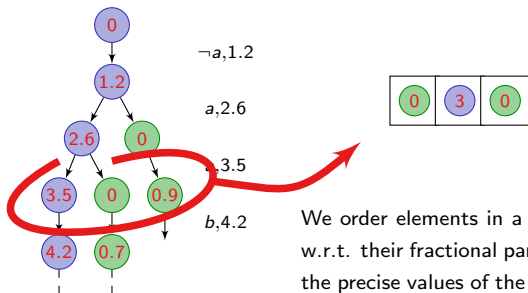
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- 👉 there is a well quasi-order on the set of abstract configurations (subword relation):

highman \sqsubseteq highmountain

Summary

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* by reduction of the recurrence problem for channel machines

[MH84] Miyano, Hayashi. Alternating finite automata on ω -words (TCS, 1984).

[OW06] Ouaknine, Worrell. On metric temporal logic and faulty Turing machines (FoSSaCS'06).

Outline

1. Introduction
2. Definition of the logics
3. The timed automaton model
4. The model-checking problem
5. Some interesting fragments
6. Conclusion

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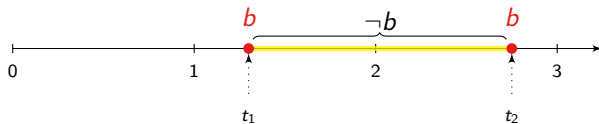
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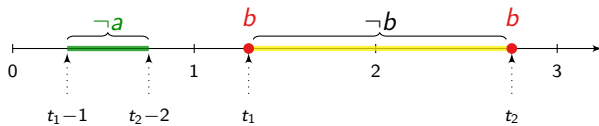
[HR04] Hirshfeld, Rabinovich. Logics for real time: decidability and complexity (Fundamenta Informaticae, 2004).

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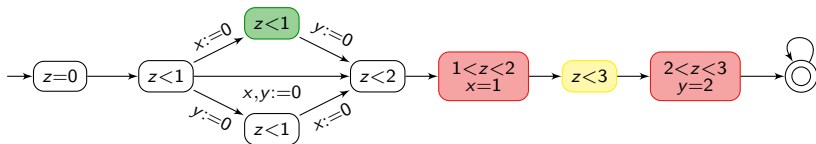
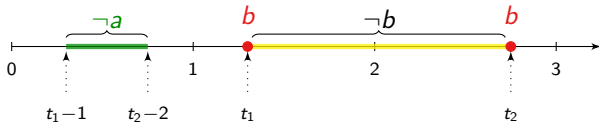
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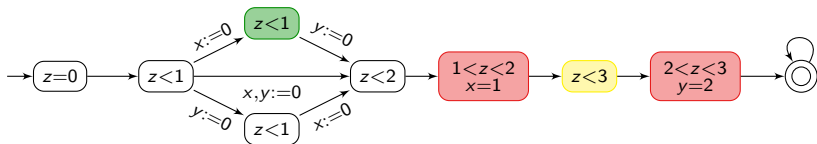
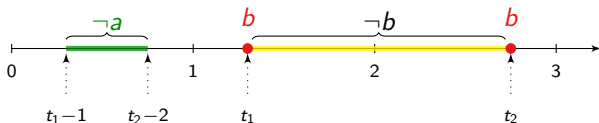
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☞ This idea can be extended to any formula in [MITL](#)

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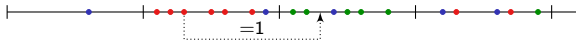
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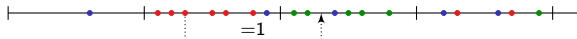
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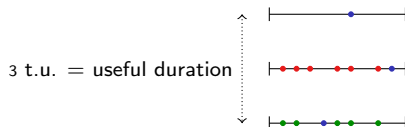
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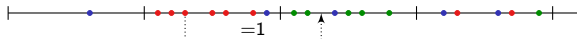
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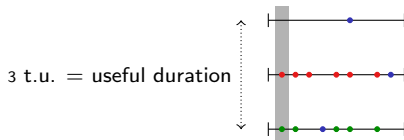
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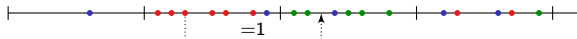
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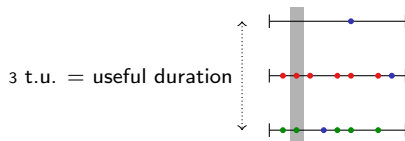
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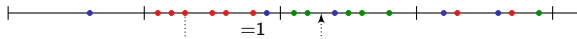
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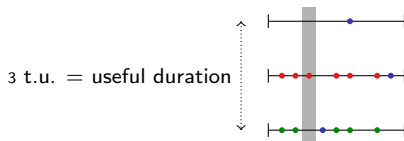
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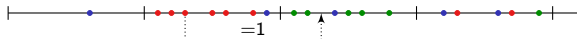
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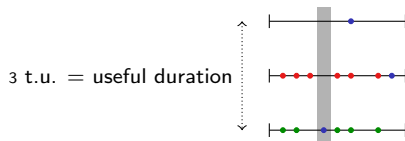
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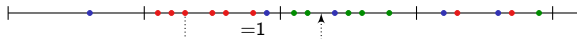
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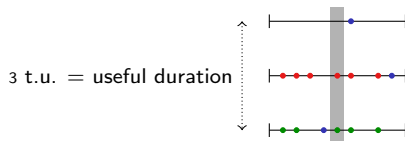
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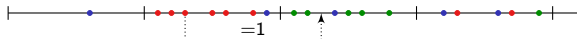
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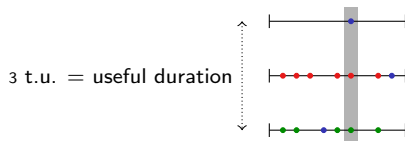
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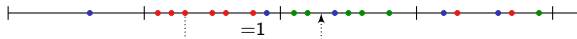
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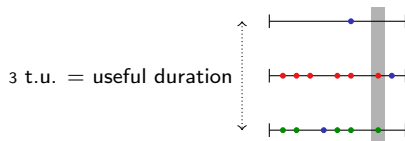
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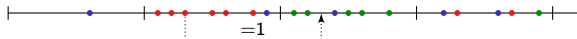
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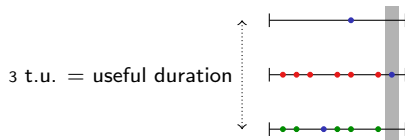
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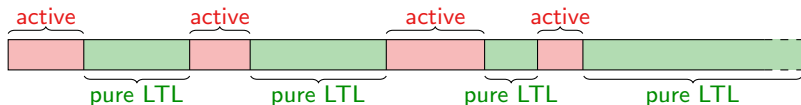


Algorithm for coFlat-MTL

$\varphi \rightsquigarrow$ alternating timed automata $\mathcal{B}_{\neg\varphi}$ for $\neg\varphi$ with a 'flatness' property

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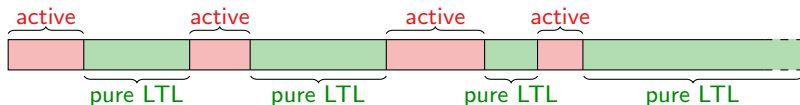
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- where - the number of active fragments is at most exponential
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- ▶ **active** fragment = cycle-bounded computation in a channel machine
- ▶ **pure LTL** part = finite automaton computation

Outline

1. Introduction
2. Definition of the logics
3. The timed automaton model
4. The model-checking problem
5. Some interesting fragments
6. Conclusion

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- ▶ An interesting phenomenon (in the continuous semantics):

	TCTL	MTL	Bounded-TCTL	Bounded-MTL
m.-c.	PSPACE	NPR/undec.	PSPACE	PSPACE
sat.	undec.	NPR/undec.	non- <i>elem.</i> *	PSPACE

* ongoing work with Jenkins, Ouaknine, Worrell.